

Programme SFAIR 15-16 november 2009 Uppsala

Date	Time	Title	Speaker		
Nov 15, 2010	14:00	Welcome	SFAIR Board		
	14:05	The FAIR project - founding status	Ö. Skeppstedt	MSL/FAIR	30+10 min
	14:45	VR-RFI view on FAIR	D. Edvardsson	VR	20+10 min
	15:15	R3B phase 0 experiments	T. Nilsson	Chalmers	25+5 min
	15:45	Coffee break			30 min
	16:15	FPGA-based trigger logics	H. Johansson	Chalmers	15+5 min
	16:35	FPGA and Sampling ADC developments	K. Fransson	UU	25+5 min
	17:05	CAI activities	V. Ziemann	UU	35+5 min
	17:45	SFAIR issues	SFAIR Board		20 min
Date	Time	Title	Speaker		
Nov 16, 2010	09:00	Developing AGATA towards FAIR	B. Cederwall	KTH	25+5 min
	09:30	Performance of the PANDA calorimeter in the low-energy regime	K. Marcks von Würtemberg	SU	25+5 min
	10:00	Pellet target for PANDA	H. Calén	UU	25+5 min
	10:30	Coffee break			30 min
	11:00	APPA programme	T. Stöhlker	GSI	45+5 min
	11:50	Tests of the new extraction in CRYRING	A. Simonsson	MSL	25+5 min